	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1	6556936.pn.	USPAT	2004/07/12 13:32
2	BRS	L2	69	emulation near plurality	USPAT	2004/07/12 13:32
3	BRS	L3	104	emulation adj processor	USPAT	2004/07/12 13:33
4	BRS	L4	5	((emulation adj processor) near plurality)	USPAT	2004/07/12 13:40
5	BRS	L5	0	((emulation adj processor) near plurality) and asychronous	USPAT	2004/07/12 13:40
6	BRS	L6	0	asychronous adj buffer	USPAT	2004/07/12 13:40
7	BRS	L7	63	asynchronous adj buffer	USPAT	2004/07/12 13:47
8	BRS	L8	0	L7 and L4	USPAT	2004/07/12 13:41
9	BRS	L9	1	L7 and emulation	USPAT	2004/07/12 13:41
10	BRS	L10	0	asynchronous adj buffer adj card	USPAT	2004/07/12 15:20
11	BRS	L11	16	(asynchronous adj buffer) and multiplex	USPAT	2004/07/12 13:43
12	BRS	L12	0	(asynchronous adj buffer) and multiplex and emulation	USPAT	2004/07/12 13:43
13	BRS	L13	3	(asynchronous adj buffer) and (multi-bit)	USPAT	2004/07/12 13:48
14	BRS	L14	0	(asynchronous adj buffer) and (multi-bit) and emulation	USPAT	2004/07/12 13:49
15	BRS	L15	0	emulation and (common near path) and multi-bit	USPAT	2004/07/12 13:49
16	BRS	L16	267	emulation and multi-bit	USPAT	2004/07/12 13:50
17	BRS	L17	2	emulation and multi-bit and multi-step	USPAT	2004/07/12 13:50
18	BRS	L18	174	emulation and (digital adj logic)	USPAT	2004/07/12 13:50
19	BRS	L19	0	emulation and (digital adj logic) and (common near path)	USPAT	2004/07/12 13:51
20	BRS	L20	124	emulation and (digital adj logic) and path	USPAT	2004/07/12 13:52
21	BRS	L21	26	emulation and (digital adj logic) and (multiple adj signals)	USPAT	2004/07/12 13:53
22	BRS	L22	0	(emulation and (multiple adj signals)).ti.	USPAT	2004/07/12 13:53

	Type	L #	Hits	Search Text	DBs	Time Stamp
23	BRS	L23	12	(emulation and signals).ti.	USPAT	2004/07/12 13:55
24	BRS	L24	0	(emulation and signals).ti. and (plurality near processors)	USPAT	2004/07/12 13:55
25	BRS	L25	9	(emulation and signals).ti. and plurality	USPAT	2004/07/12 13:57
26	BRS	L26	4	(emulation and signals).ti. and plurality and clocks	USPAT	2004/07/12 14:00
27	BRS	L28	1	(emulation and signals).ti. and plurality and clocks and path	USPAT	2004/07/12 14:00
28	BRS	L27	4	(emulation and signals).ti. and plurality and clocks	USPAT	2004/07/12 14:05
29	BRS	L29	0	(common and path).ti. and (digital adj logic)	USPAT	2004/07/12 14:02
30	BRS	L30	70	(common and path).ti.	USPAT	2004/07/12 14:02
31	BRS	L31	7732	(common and path).ti. emulation	USPAT	2004/07/12 14:02
32	BRS	L32	0	(common and path).ti. and emulation	USPAT	2004/07/12 14:02
33	BRS	L33	2	(emulation and signals).ti. and plurality and clocks and buffer	USPAT	2004/07/12 14:05
34	BRS	L34	5	(asynchronous and emulation).ti.	USPAT	2004/07/12 15:21
35	BRS	L35	5	(asynchronous and emulation).ti. and emulation	USPAT	2004/07/12 15:25
36	BRS	L36	3	(asynchronous and emulation).ti. and emulation and circuit	USPAT	2004/07/12 15:22
37	BRS	L37	3	(asynchronous and emulation).ti. and emulation and logic	USPAT	2004/07/12 15:22
38	BRS	L38	0	(asynchronous and emulation and logic).ti.	USPAT	2004/07/12 15:25
39	BRS	L39	11	asynchronous and (emulation and logic).ti.	USPAT	2004/07/12 15:25

07/12/2004, EAST Version: 1.4.1